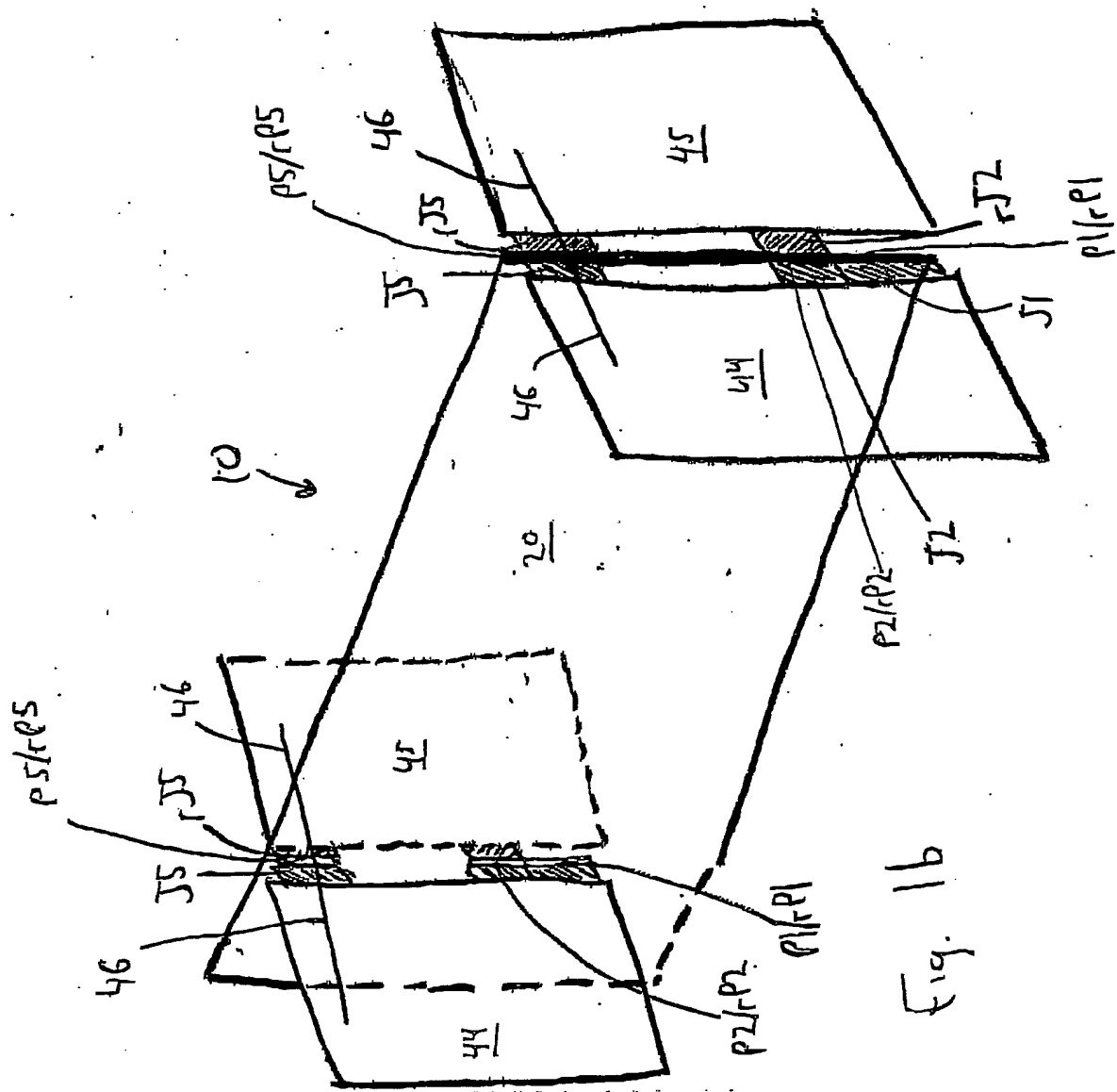

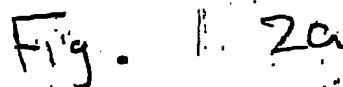


Fig. 1a







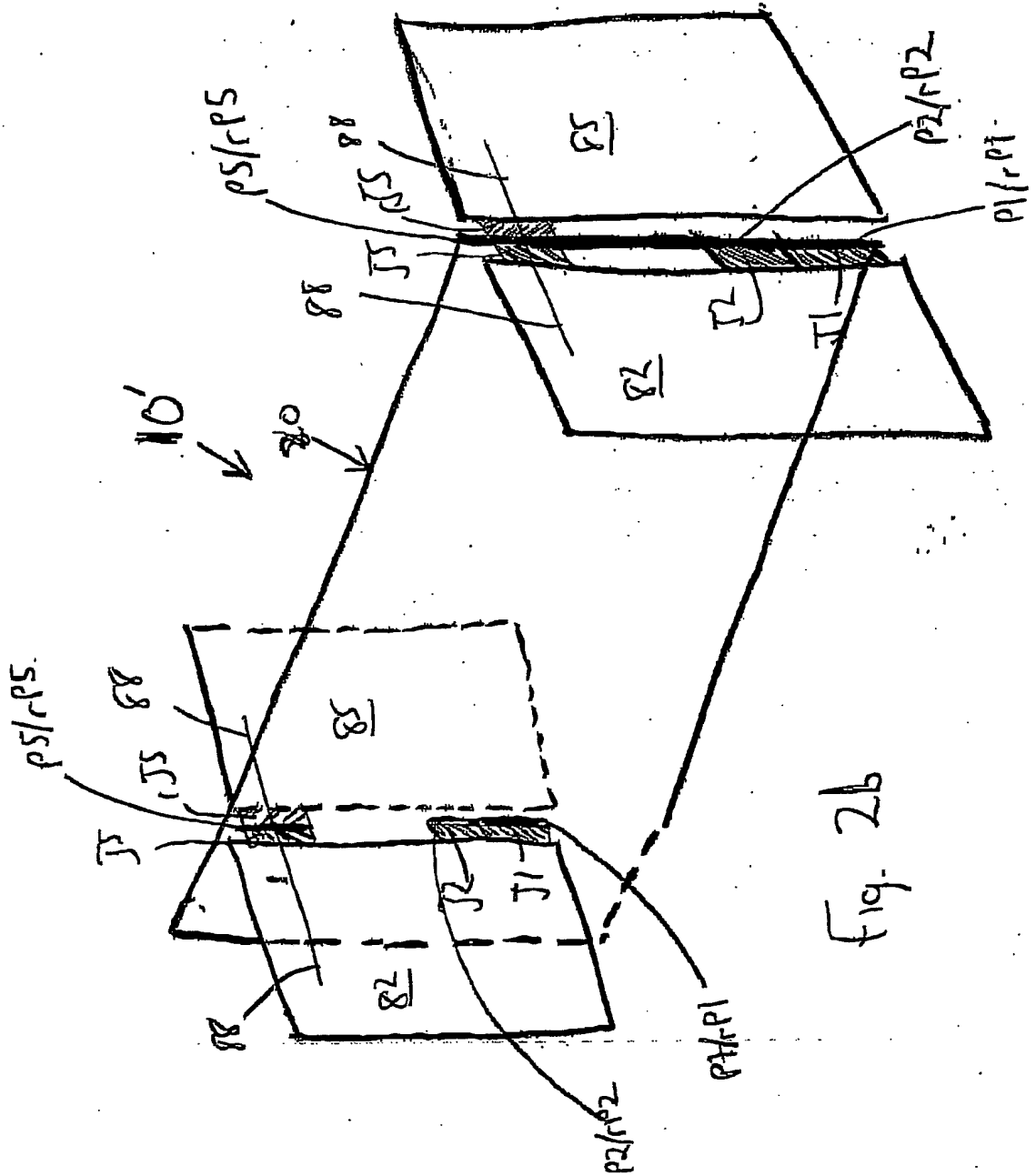


Fig. 2b

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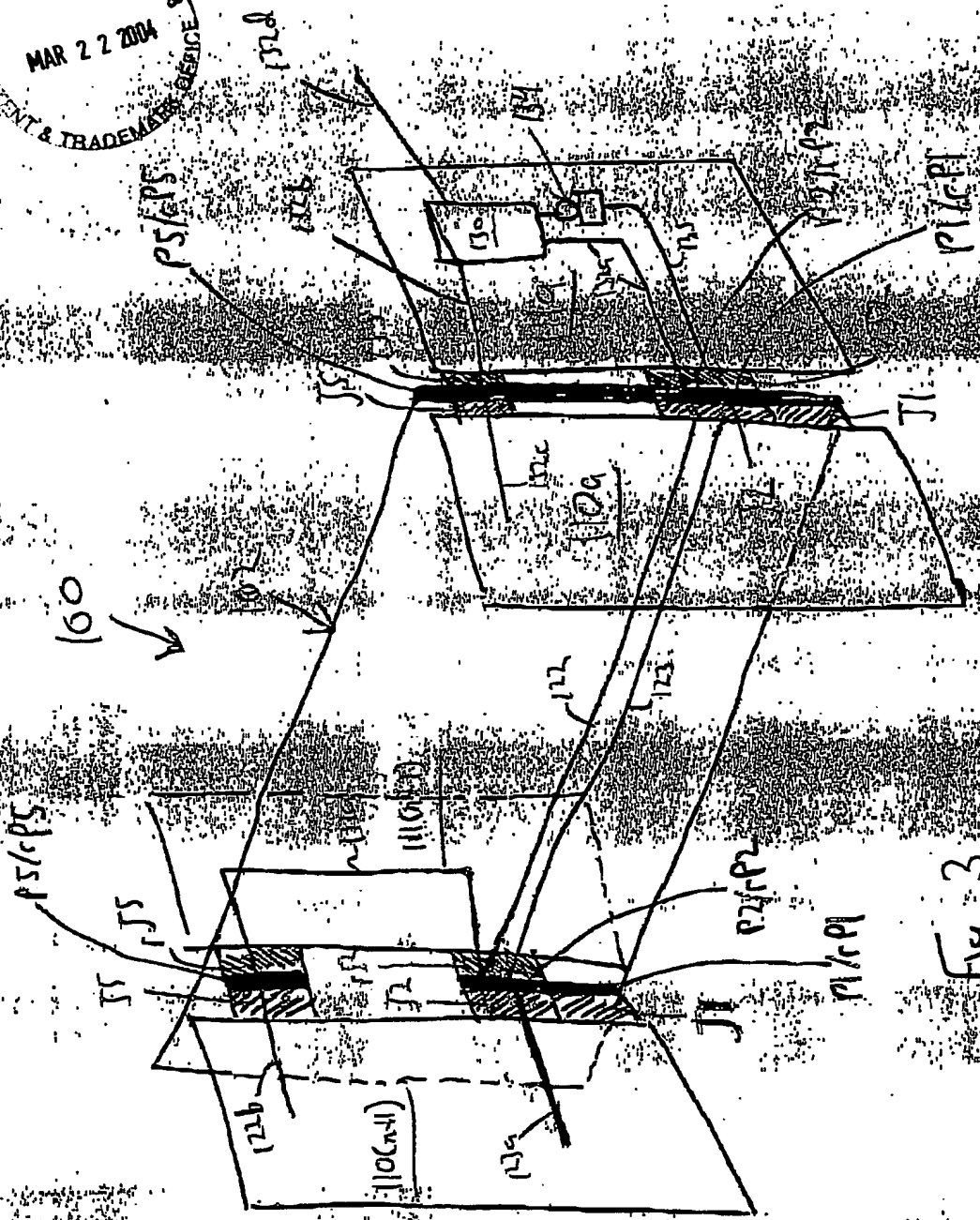


Fig. 3

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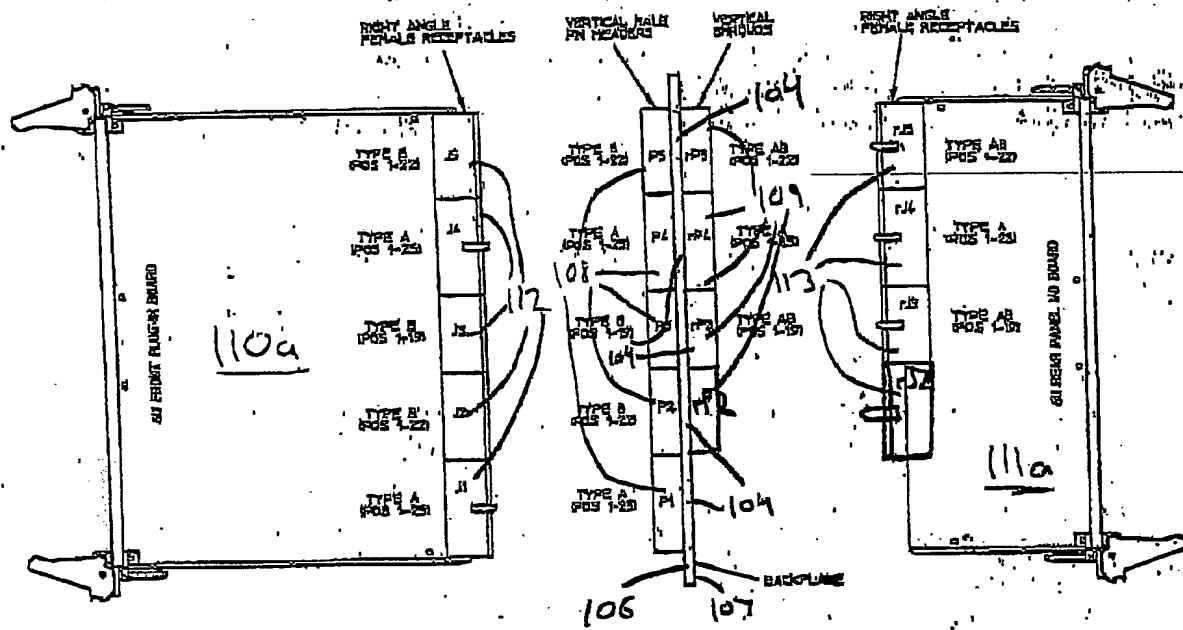


Fig. 4

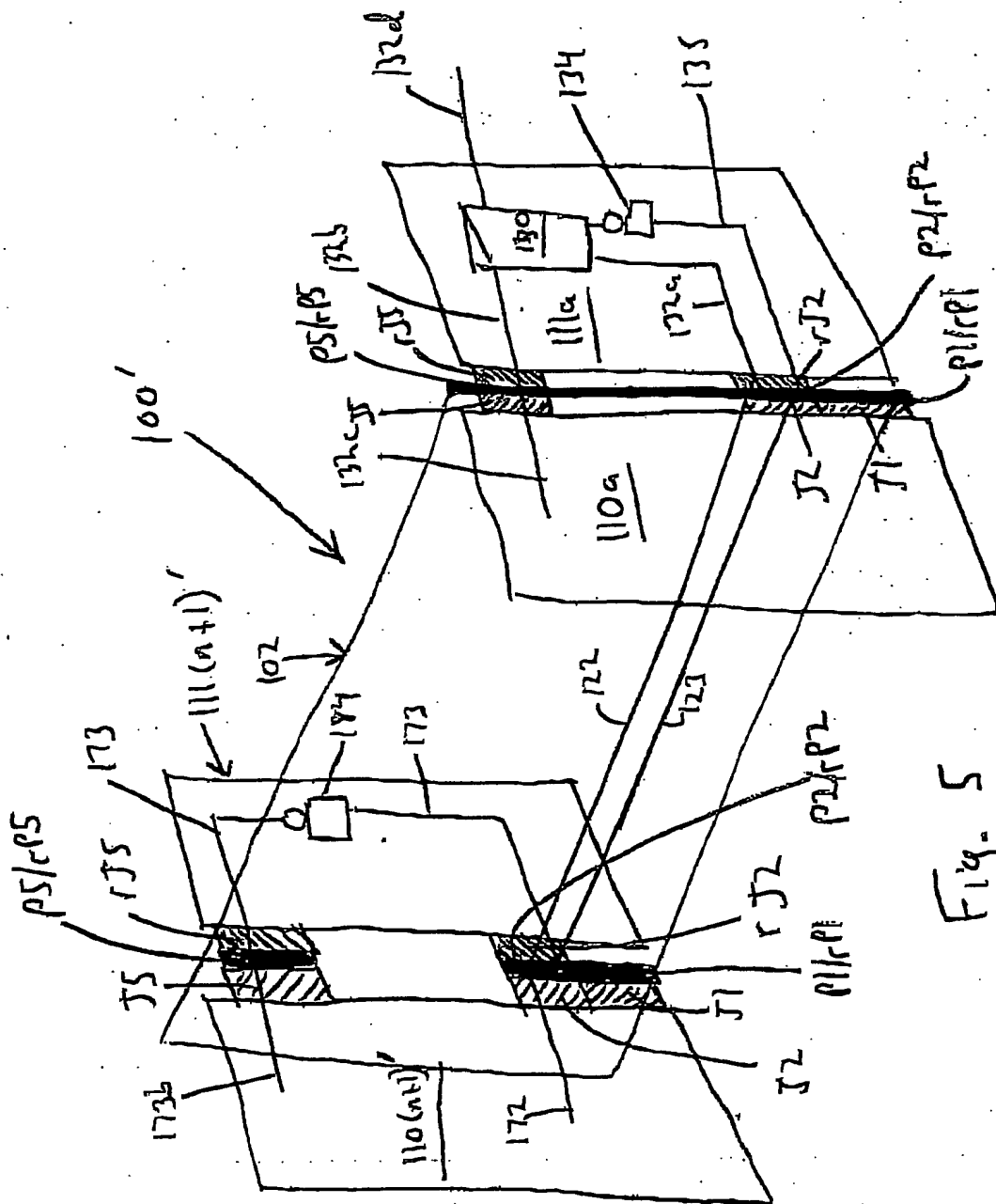


Fig. 5

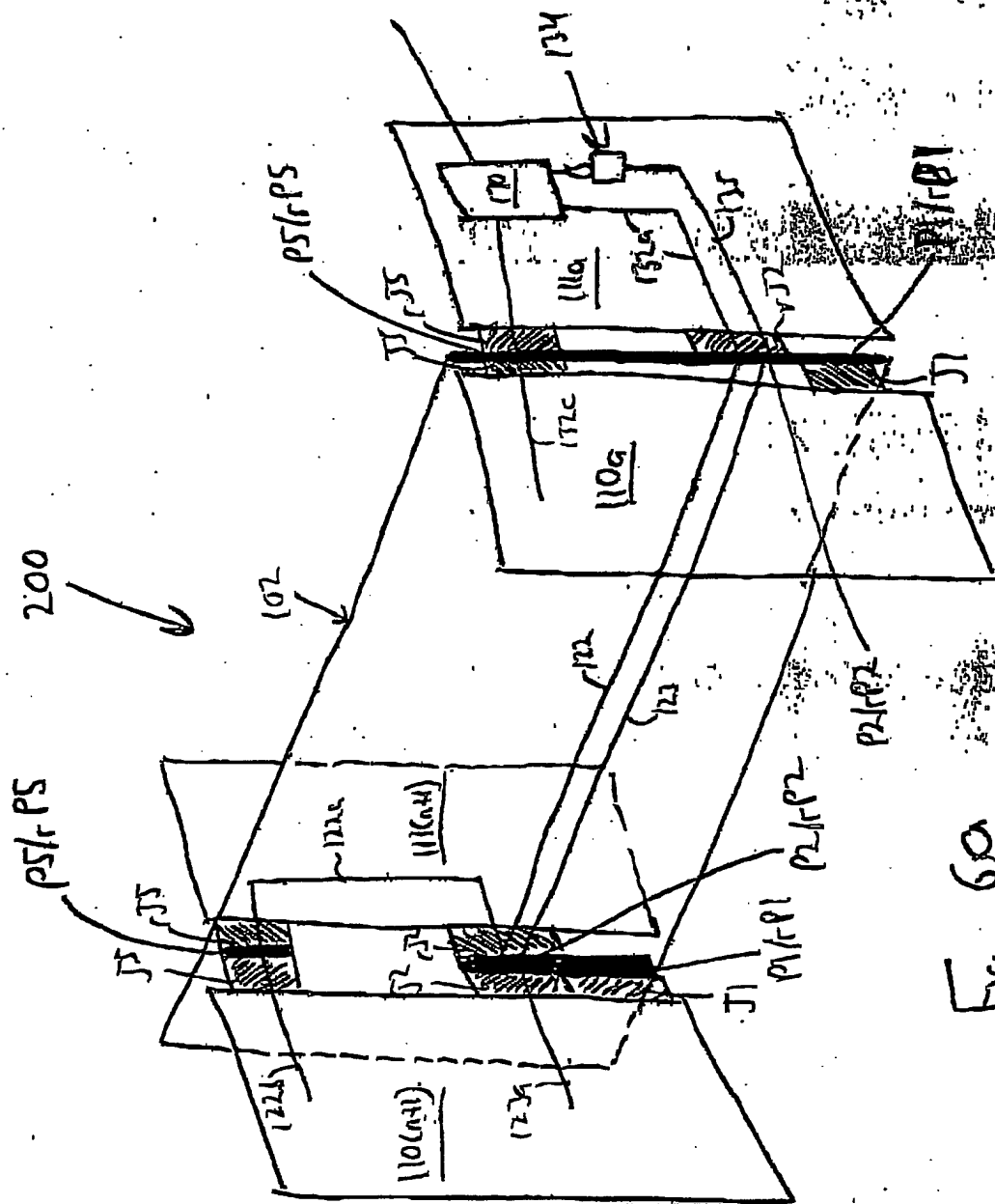
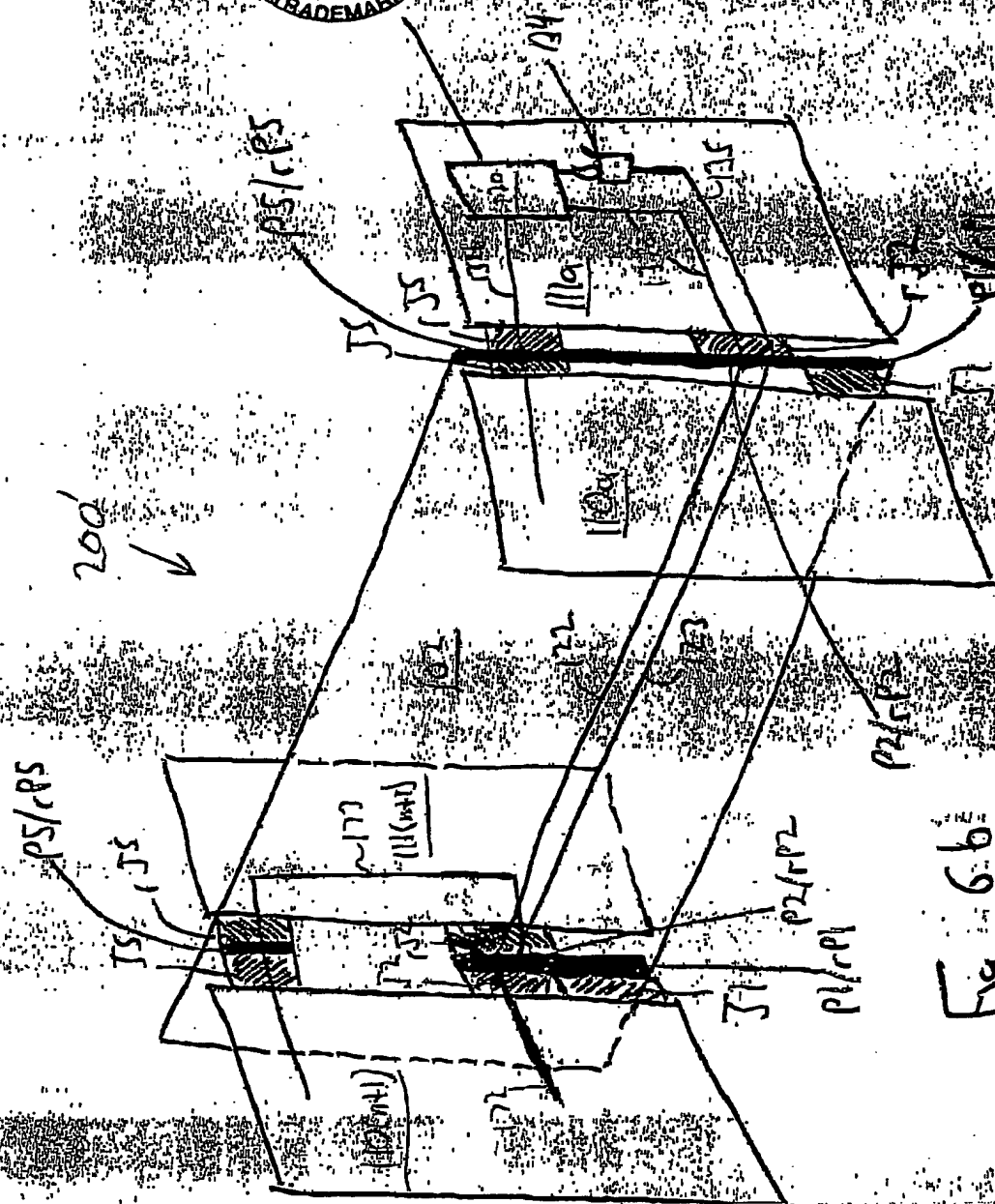


Fig. 60

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Pin Assignments

Pin	A	B	C	D	E	F
22	GND	GA4 ⁽¹⁵⁾	GA3 ⁽¹²⁾	GA2 ⁽¹⁰⁾	GA1 ⁽¹³⁾	GA0 ⁽¹⁶⁾
21	GND	RSV	RSV	RSV	RSV	GND
20	GND	RSV	RSV	RSV	GND	RSV
19	GND	RSV	RSV	RSV	RSV	GND
18	GND	BR5VP2A18	BR5VP2B18	BR5VP2C18	GND	BR5VP2E18
17	GND	BR5VP2A17	GND	RSV	RSV	GND
16	GND	BR5VP2A16	BR5VP2B16	RSV	GND	BR5VP2E16
15	GND	BR5VP2A15	GND	RSV	RSV	GND
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]
13	GND	AD[36]	GND	V(I/O) ⁽²⁾	AD[37]	AD[38]
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]
11	GND	AD[45]	GND	V(I/O) ⁽²⁾	AD[44]	AD[43]
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]
9	GND	AD[62]	GND	V(I/O) ⁽²⁾	AD[51]	AD[50]
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]
7	GND	AD[59]	GND	V(I/O) ⁽²⁾	AD[58]	AD[57]
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]
5	GND	C/BE[5]#	GND	V(I/O) ⁽²⁾	C/BE[4]#	PAR64
4	GND	V(I/O) ⁽¹⁾	BR5VP2B4	C/BE[7]#	GND	C/BE[6]#
3 ⁽³⁾	GND	RSV	GND	RSV	RSV	RSV
2 ⁽³⁾	GND	RSV	RSV	UNC ⁽⁴⁾	RSV	RSV
1 ⁽³⁾	GND	RSV	GND	RSV	RSV	GND
25	GND	5V	REQ64#	ENUM#	3.3V	5V
24	GND	AD[1]	5V	AD[2]	AD[0]	ACK64#
23	GND	3.3V	AD[4]	AD[3]	AD[5]	AD[7]
22	GND	AD[7]	GND	AD[6]	AD[6]	AD[5]
21	GND	3.3V	AD[9]	AD[8]	M6SEN ⁽¹⁰⁾	C/BE[0]#
20	GND	AD[12]	GND	V(I/O) ⁽²⁾	AD[11]	AD[10]
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]
18	GND	SERR#	GND	3.3V	PAR	C/BE[17]#
17	GND	3.3V	IPMB SCL	IPMB SDA	C/BE[18]#	SERR#
16	GND	DEVSEL#	GND	V(I/O) ⁽²⁾	STOP#	LOCK#
15	GND	3.3V	FRAME#	IRDY#	RD SEL ⁽¹⁷⁾	TRDY#
12-14	KEY AREA					
11	GND	AD[19]	AD[17]	AD[16]	GND	C/BE[2]#
10	GND	AD[21]	GND	3.3V	AD[20]	AD[18]
9	GND	C/BE[3]#	IDSEL ⁽¹⁾	AD[23]	GND	AD[22]
8	GND	AD[25]	GND	V(I/O) ⁽²⁾	AD[25]	AD[24]
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]
6	GND	REQ#	GND	3.3V	CLK	AD[31]
5	GND	BR5VP1A5	BR5VP1B5	RST#	GND	GNT#
4	GND	IPMB PWR	HEALTHY# ⁽¹⁶⁾	INTC#	INTP	INTS
3	GND	INTA#	INTB#	INTC#	GND	INTD#
2	GND	TRCK ⁽¹⁵⁾	5V	TMS ⁽¹⁴⁾	TRD ⁽¹⁵⁾	TDI ⁽¹⁶⁾
1	GND	5V	-12V	TRST# ⁽¹⁴⁾	+12V	5V

 = long pins (front only) = short pins (front only) = medium length pins (front only)

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Fig. 7